

Amendments to the Specification

Please amend paragraph 65 (on page 18) as follows:

[0065] FIG. 5 shows exemplary values of normalized output-stage bias current I_{b2} for use with varying power-amplifier operating modes. For each of these curves, V_{cc} is 3.0 volts, source resistances are nominally 50Ω , and ~~mid-frequency low-frequency~~ transistor current gains are nominally 80-dB.